
EOL-less Quintus

First steps towards bringing mainline Linux to the Volla Phone
Quintus

Ivaylo Ivanov, Muhammad Asif

Table of contents

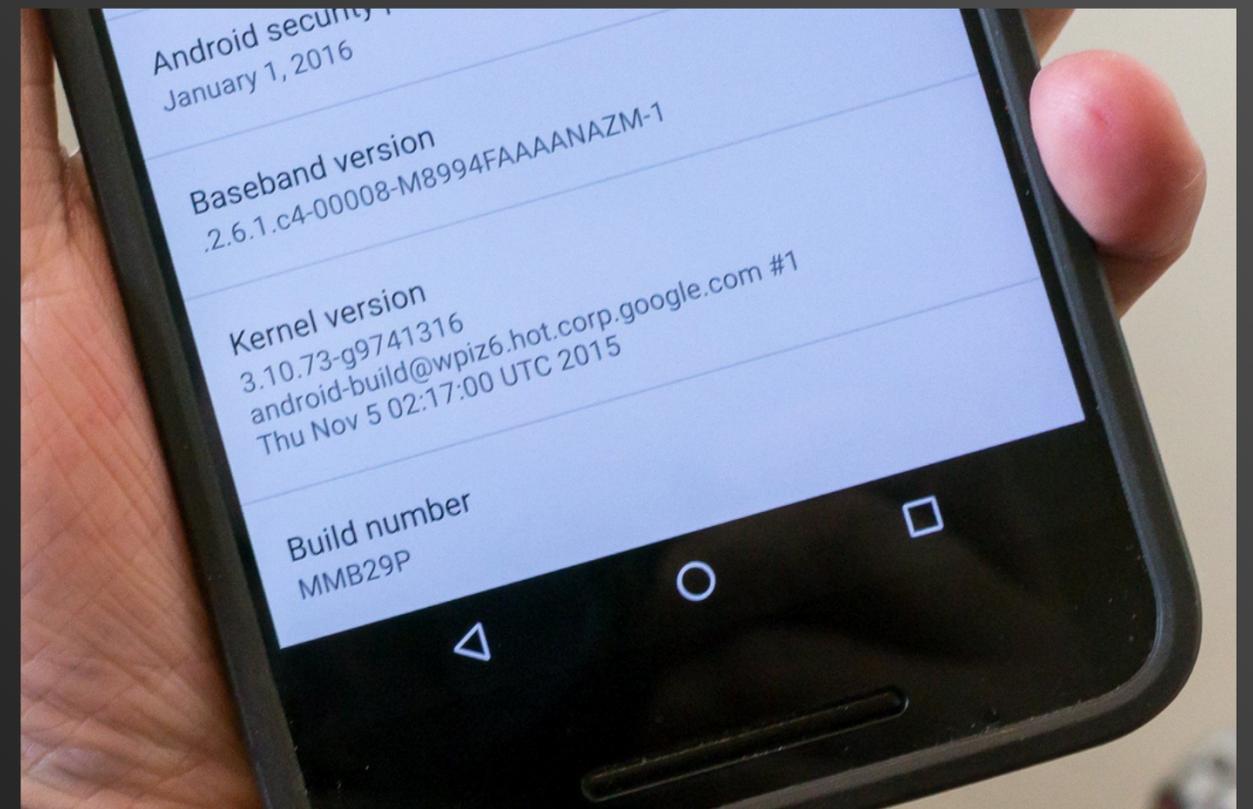
1. Introduction to the problem with mass-production android phones
2. What can we do about it?
3. First steps towards becoming EOL-less
4. What works and what needs to be done
5. ROADMAP



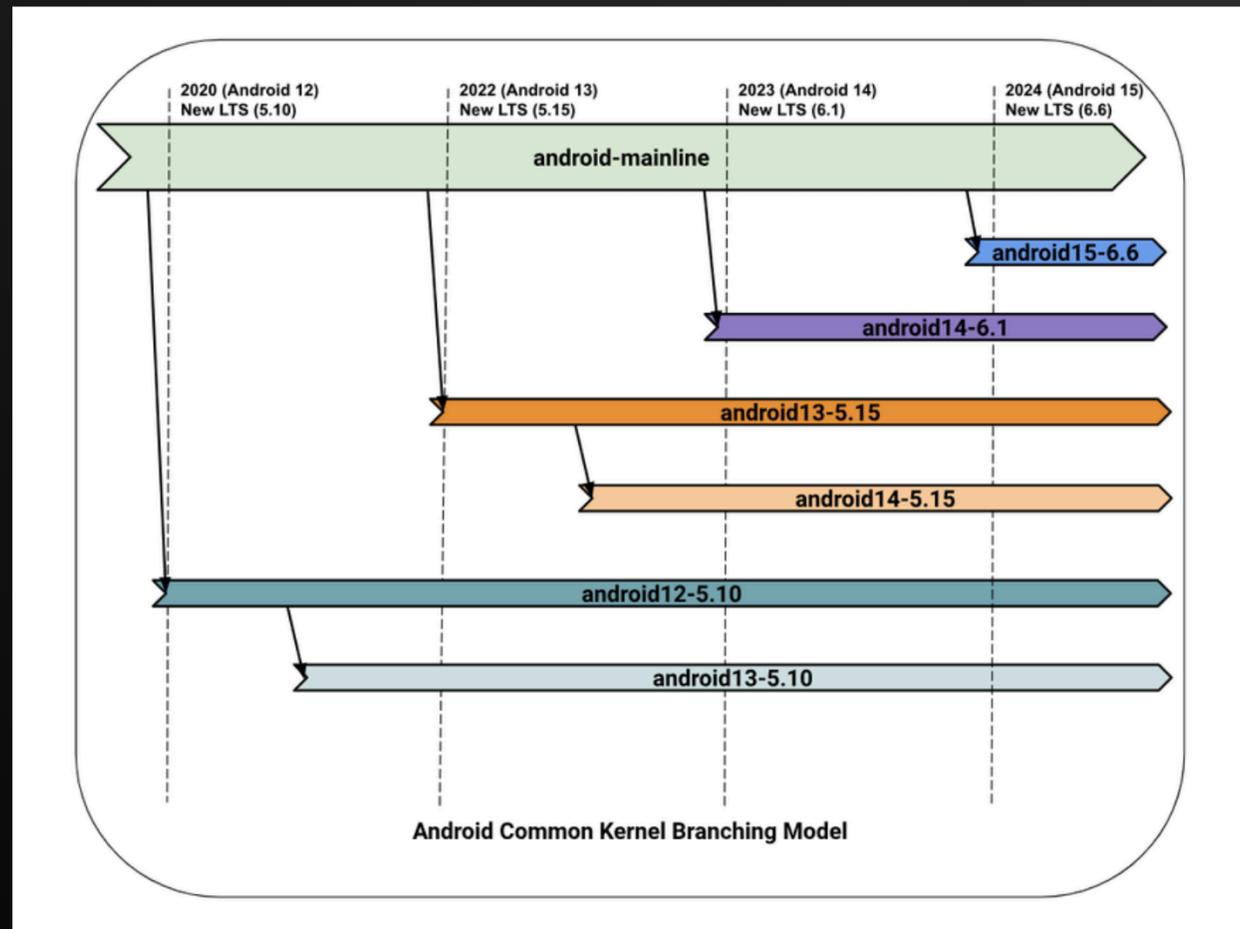
1. Introduction to the problem with mass-production phones

The problem

- Most phones nowadays ship with either iOS or Android
- The ones that ship with Android make use of the Linux kernel, unlike iOS phones
- Due to the nature of Android, it requires extensive patches to the Linux kernel, which are organized by Google

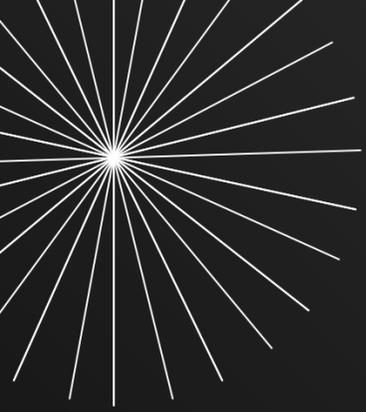


The problem



Lifetime of Android kernels

- Phone OEMs use BSPs provided by the SoC vendor, which include stuff like late-stages of bootloader, kernel, TZ, userspace trees, etc
- SoC vendors fork the linux kernel with android patches from Google (officially called ACK), which is forked from torvalds/linux



The problem

- SoC manufacturers have to go through an extensive amount of development stages, which may take multiple years and then hand-off the already-old codebase to OEMs
- Due to Google's relaxed requirements for kernel versions in the past, SoC vendors never bothered to catch up with newer Linux versions

arm64: dts: gs101: add exynos reboot and pmu syscon node

Youngmin Nam authored and Will McVicker committed on Aug 8, 2020

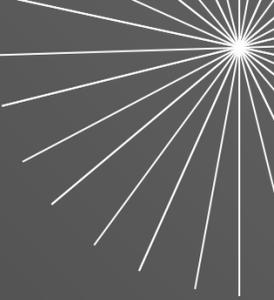
arm64: dts: initial device tree for GS101

HYUNKI-KOO authored and Will McVicker committed on Aug 8, 2020

End of commit history for this file

When did Pixel 6 come out?

October 28, 2021



The problem

```
1 # SPDX-License-Identifier: GPL-2.0
2 VERSION = 4
3 PATCHLEVEL = 19
4 SUBLEVEL = 191
5 EXTRAVERSION =
6 NAME = "People's Front"
7
8 # *DOCUMENTATION*
```

Kernel version of Quintus;
Phone was released in October 2024
Kernel was released in October 2018
We are at 6.16rc1

- Although that has changed with Google forcing much newer kernel versions for SoC vendors, it still does not fix the dependencies on SoC vendors for drivers
- For example, a kernel upgrade for Qualcomm or MediaTek chipset costs a lot of money that could've been spent on R&D

The problem

- This is not a sustainable situation, as it leaves the SoC vendor in charge of upgrading the kernel source
- SoC vendors also don't entertain B-tier and C-tier OEMs/ODMs, because from the vendor's point of view, they are not as much of a money source as A-tier OEMs.

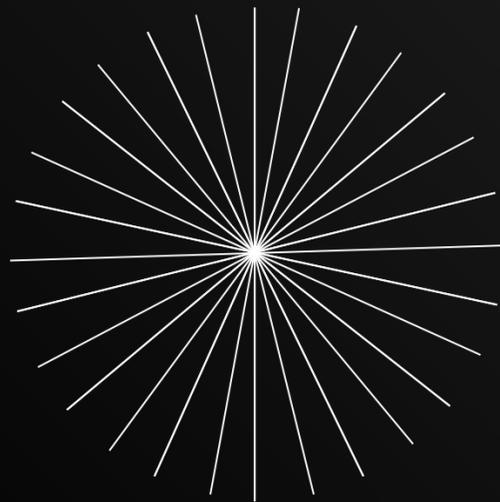


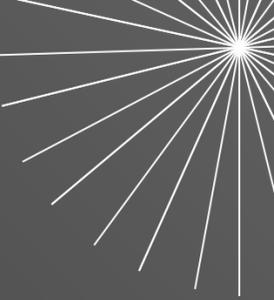
2. What can we do
about it?



“If you want a thing done well, do it
yourself”

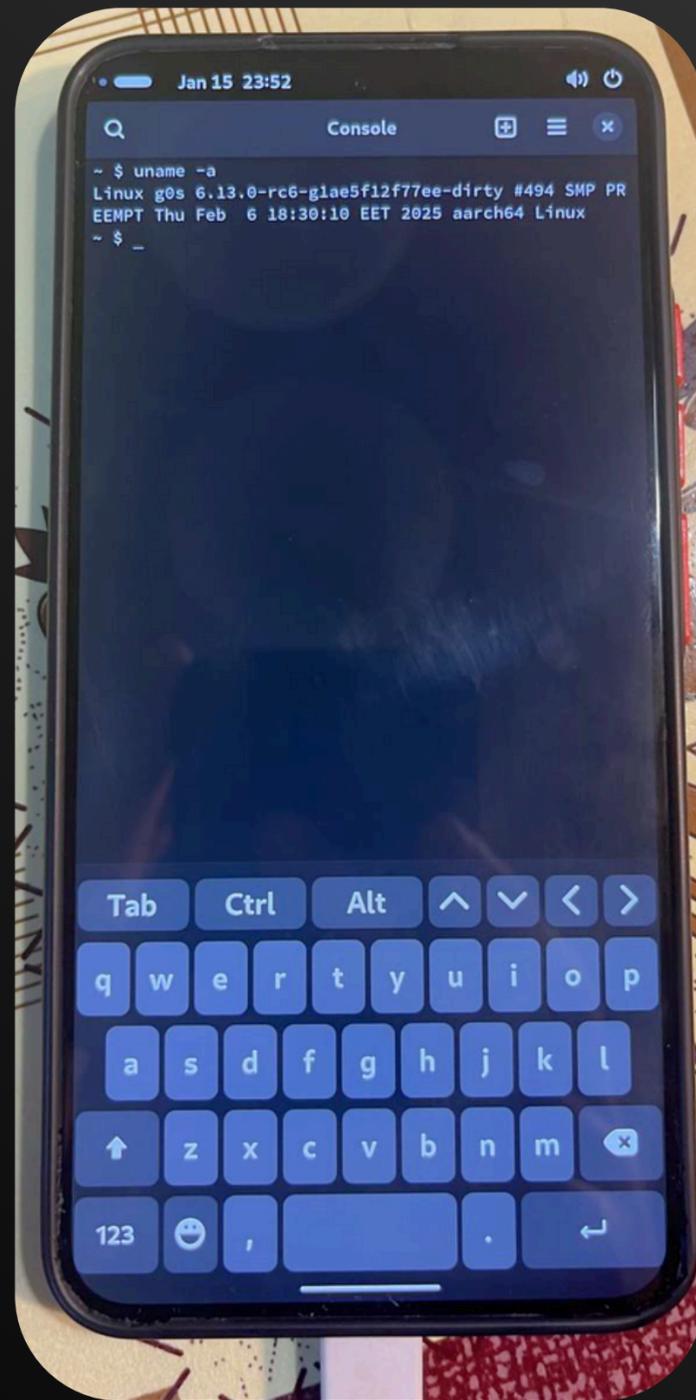
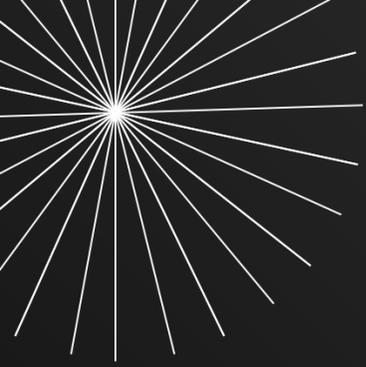
~ Napoleon Bonaparte





What can we do

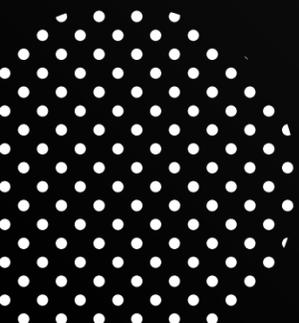
- As normal users who are not affiliated with companies, there is no easy route to achieving a longer lifespan.
- Community projects that exist to try and solve the EOL problem:
 1. PostmarketOS
 2. Ubuntu Touch
 3. Droidian/Mobian
- Most of the devices supported by these projects use the stock OEM kernel (downstream), which relies on extensive prebuilt blobs linked to the Android userspace and libc implementation (bionic)
- Hybris is another example of a project that aims to run GNU/Linux with Android blobs, but its effectiveness steadily decreases as Linux programs use newer features only available in recent kernels



What can we do

- There are really only two options:
 1. Use the vendor-provided kernel with a Linux userspace, which will extend the life of the device for a few more years (until the kernel becomes too outdated)
 2. Spend a lot of time and effort getting the mainline Linux kernel to work on the device.

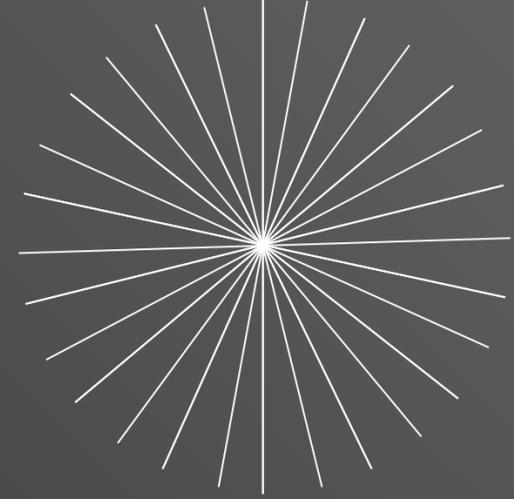
Galaxy S22+ running mainline Linux with framebuffer, usb and touchscreen





3. First steps towards becoming EOL-less

The Mainline Way!



```
[net-next v1] net: ethernet: mtk_eth_soc:
support named IRQs
2025-06-14 17:31 UTC (2+ messages)

[PATCH 00/28] iio: zero init stack with { }
instead of memset()
2025-06-14 14:18 UTC (46+ messages)
` [PATCH 01/28] iio: accel: adxl372: use = "
` [PATCH 02/28] iio: accel: msa311: "
` [PATCH 03/28] iio: adc: dln2-adc: "
` [PATCH 04/28] iio: adc: mt6360-adc: "
` [PATCH 05/28] iio: adc: rockchip_saradc: "
` [PATCH 06/28] iio: adc: rtq6056: "
` [PATCH 07/28] iio: adc: stm32-adc: "
` [PATCH 08/28] iio: adc: ti-ads1015: "
` [PATCH 09/28] iio: adc: ti-ads1119: "
` [PATCH 10/28] iio: adc: ti-imp92064: "
` [PATCH 11/28] iio: adc: ti-tsc2046: "
```

- The mainline kernel is the basis for all SoC vendor kernels, albeit forked to oblivion.
- The most sustainable solution to the EOL problem is to implement and upstream support for the device straight to the mainline Linux kernel and other userspace firmware like MESA. From there on, distributions like Ubuntu, paired with proper mobile interface (ex. Phosh), can be used.

The Mainline Way!

- It takes a lot of time to get non-QCOM hardware in upstream due the to sheer amount of work required. But it's worth it!

2025-02-05	arm64: dts: exynos8895-dreamlte: enable support for the touchscreen	Ivaylo Ivanov	1	-0/+40
2025-02-05	arm64: dts: exynos8895-dreamlte: enable support for microSD storage	Ivaylo Ivanov	1	-0/+32
2025-02-05	arm64: dts: exynos8895: add a node for mmc	Ivaylo Ivanov	1	-0/+16
2025-02-05	arm64: dts: exynos8895: define all usi nodes	Ivaylo Ivanov	1	-0/+868
2025-02-05	arm64: dts: exynos8895: add syscon nodes for peric0/1 and fsys0/1	Ivaylo Ivanov	1	-0/+24
2025-02-05	soc: samsung: usi: implement support for USlv1 and exynos8895	Ivaylo Ivanov	1	-13/+58
2025-02-05	soc: samsung: usi: add a routine for unconfiguring the ip	Ivaylo Ivanov	1	-0/+28
2025-02-05	dt-bindings: soc: samsung: usi: add USlv1 and samsung,exynos8895-usi	Ivaylo Ivanov	2	-37/+79
2025-01-13	dt-bindings: mmc: samsung,exynos-dw-mshc: add specific compatible for exynos8895	Ivaylo Ivanov	1	-0/+1
2025-01-07	i2c: exynos5: Add support for Exynos8895 SoC	Ivaylo Ivanov	1	-4/+31
2025-01-07	dt-bindings: i2c: exynos5: Add samsung,exynos8895-hsi2c compatible	Ivaylo Ivanov	1	-0/+1
2025-01-05	dt-bindings: soc: samsung: exynos-sysreg: add sysreg compatibles for exynos8895	Ivaylo Ivanov	1	-0/+8
2024-12-30	arm64: dts: exynos8895: Add camera hsi2c nodes	Ivaylo Ivanov	1	-0/+44
2024-12-14	arm64: dts: exynos8895: Add a PMU node for the second cluster	Ivaylo Ivanov	1	-1/+11
2024-12-02	arm64: dts: exynos8895: Add serial_0/1 nodes	Ivaylo Ivanov	1	-0/+26
2024-11-14	dt-bindings: clock: actions,owl-cmu: convert to YAML	Ivaylo Ivanov	3	-53/+61
2024-11-13	dt-bindings: timer: actions,owl-timer: convert to YAML	Ivaylo Ivanov	3	-22/+108
2024-11-04	tty: serial: samsung: Add Exynos8895 compatible	Ivaylo Ivanov	1	-0/+13
2024-11-04	dt-bindings: serial: samsung: Add samsung,exynos8895-uart compatible	Ivaylo Ivanov	1	-2/+12
2024-10-26	arm64: dts: exynos8895: Add spi_0/1 nodes	Ivaylo Ivanov	1	-0/+30
2024-10-26	arm64: dts: exynos8895: Add Multi Core Timer (MCT) node	Ivaylo Ivanov	1	-0/+20
2024-10-26	arm64: dts: exynos8895: Add clock management unit nodes	Ivaylo Ivanov	1	-0/+85
2024-10-26	dt-bindings: timer: exynos4210-mct: Add samsung,exynos8895-mct compatible	Ivaylo Ivanov	1	-0/+2
2024-10-26	clk: samsung: Introduce Exynos8895 clock driver	Ivaylo Ivanov	2	-0/+2804
2024-10-26	clk: samsung: clk-pll: Add support for pll_{1051x,1052x}	Ivaylo Ivanov	2	-0/+4
2024-10-26	dt-bindings: clock: samsung: Add Exynos8895 SoC	Ivaylo Ivanov	2	-0/+692
2024-10-21	spi: dt-bindings: samsung: Add a compatible for samsung,exynos8895-spi	Ivaylo Ivanov	1	-0/+4
2024-10-02	soc: samsung: exynos-chipid: add exynos8895 SoC support	Ivaylo Ivanov	1	-0/+1
2024-10-02	dt-bindings: hwno: samsung,exynos-chipid: add exynos8895 compatible	Ivaylo Ivanov	1	-0/+1
2024-10-02	arm64: dts: exynos: Add initial support for Samsung Galaxy S8	Ivaylo Ivanov	2	-0/+127
2024-10-02	arm64: dts: exynos: Add initial support for exynos8895 SoC	Ivaylo Ivanov	2	-0/+1345
2024-10-02	dt-bindings: soc: samsung: exynos-pmu: Add exynos8895 compatible	Ivaylo Ivanov	1	-0/+1
2024-10-02	dt-bindings: arm: samsung: Document dreamlte board binding	Ivaylo Ivanov	1	-0/+6
2024-10-02	pinctrl: samsung: Add exynos8895 SoC pinctrl configuration	Ivaylo Ivanov	4	-0/+150
2024-10-02	dt-bindings: pinctrl: samsung: add exynos8895-wakeup-eint compatible	Ivaylo Ivanov	1	-0/+1
2024-10-02	dt-bindings: pinctrl: samsung: Add compatible for Exynos8895 SoC	Ivaylo Ivanov	1	-0/+1
2024-10-02	dt-bindings: arm: cpus: Add Samsung Mongoose M2	Ivaylo Ivanov	1	-0/+1

First steps towards becoming EOL-less

- Coming back to the topic, our target of interest is Volla's flagship phone, the **Quintus!**
- It all started from us liking Volla's openness and Linux and privacy orientation
- They were kind enough to donate us a phone in March that we could start developing on.



First steps towards becoming EOL-less

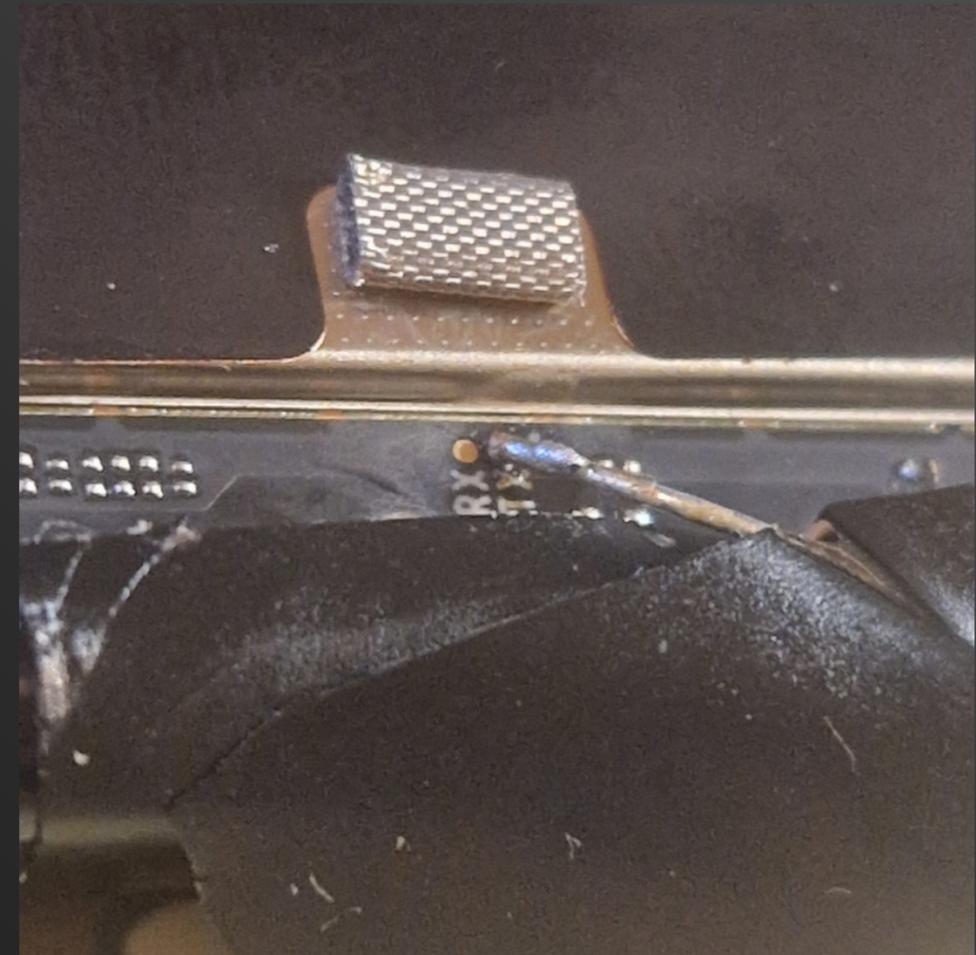


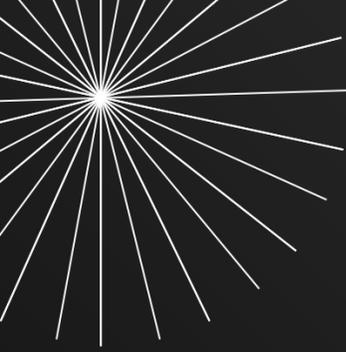
Disassembled Volla Phone Quintus

- So we began working on it!
- Getting basic mainline Linux with only 8 cores and some way of debugging was the priority
- Due to the ugliness of the stock MediaTek bootloader (LK), we had to add support for Quintus to some custom shim bootloader in order to avoid the forced DTB overlaying.

First steps towards becoming EOL-less

- In April, support for Quintus to uniLoader was added [1]. We also have a fork of U-Boot working.
- The preferred way of debugging is using UART. The Quintus exposes small RX and TX pins on the left part of the motherboard.





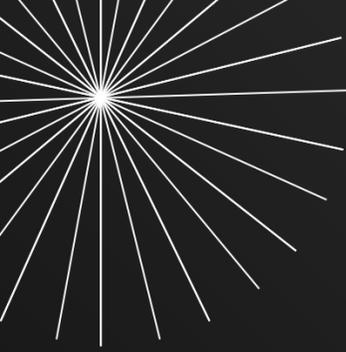
First steps towards becoming EOL-less

```
Uncompressing kernel image to 42000000
Moving Image from 0x42080000 to 0x42200000, end=43e40000
Loading Device Tree to 000000005e73d000, end 000000005e741aaf ... OK
Working FDT set to 5e73d000

Starting kernel ...

[ 0.000000] Booting Linux on physical CPU 0x000000000 [0x412fd050]
[ 0.000000] Linux version 6.14.0-rc5-next-20250306-g565351ae7e0c-dirty (ivaylo@ivaylo-T580) (aarch64-linux-gnu-gcc (Ubuntu 13.3.0-6ubuntu2-24.04) 13.3.0, GNU ld (GNU Binutils for Ubuntu)
2.42) #14 SMP PREEMPT Sat Apr  5 00:14:07 EEST 2025
[ 0.000000] KASLR disabled due to lack of seed
[ 0.000000] Machine model: Volla Phone Quintus
[ 0.000000] earlycon: uart8250 at MMIO32 0x0000000011002000 (options '')
[ 0.000000] printk: legacy bootconsole [uart8250] enabled
[ 0.000000] efi: UEFI not found.
[ 0.000000] OF: reserved mem: Reserved memory: No reserved-memory node in the DT
[ 0.000000] NUMA: Faking a node at [mem 0x0000000040000000-0x000000005fffffff]
[ 0.000000] NODE_DATA(0) allocated [mem 0x5fef5f80-0x5fef85bf]
[ 0.000000] Zone ranges:
[ 0.000000]   DMA [mem 0x0000000040000000-0x000000005fffffff]
[ 0.000000]   DMA32 empty
[ 0.000000]   Normal empty
[ 0.000000] Movable zone start for each node
[ 0.000000] Early memory node ranges
[ 0.000000]   node 0: [mem 0x0000000040000000-0x000000005fffffff]
[ 0.000000] Initmem setup node 0 [mem 0x0000000040000000-0x000000005fffffff]
[ 0.000000] cma: Reserved 32 MiB at 0x0000000000000000
[ 0.000000] psci: probing for conduit method from DT.
[ 0.000000] psci: PSCIv1.1 detected in firmware.
[ 0.000000] psci: Using standard PSCI v0.2 function IDs
[ 0.000000] psci: MIGRATE_INFO_TYPE not supported.
[ 0.000000] psci: SMC Calling Convention v1.1
[ 0.000000] percpu: Embedded 23 pages/cpu s54104 r8192 d31912 u94208
[ 0.000000] Detected VIPT I-cache on CPU0
[ 0.000000] CPU features: detected: GIC system register CPU interface
[ 0.000000] CPU features: detected: ARM errata 1165522, 1319367, or 1530923
```

After a few days of working on it, we had Linux v6.14-rc5 booting up to initramfs. More things are documented at [ivoszbg's website \[2\]](#)



First steps towards becoming EOL-less

- Unfortunately, on the same night after getting mainline booting, something REALLY unfortunate happened. The TX pad of our unit lifted and we were no more able to get any logs and debug.
- We notified Volla and while waiting for a response, we started trying to figure out alternative ways to debug.



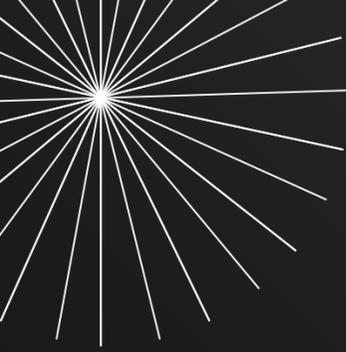
First steps towards becoming EOL-less



Framebuffer writes in LK bootloader

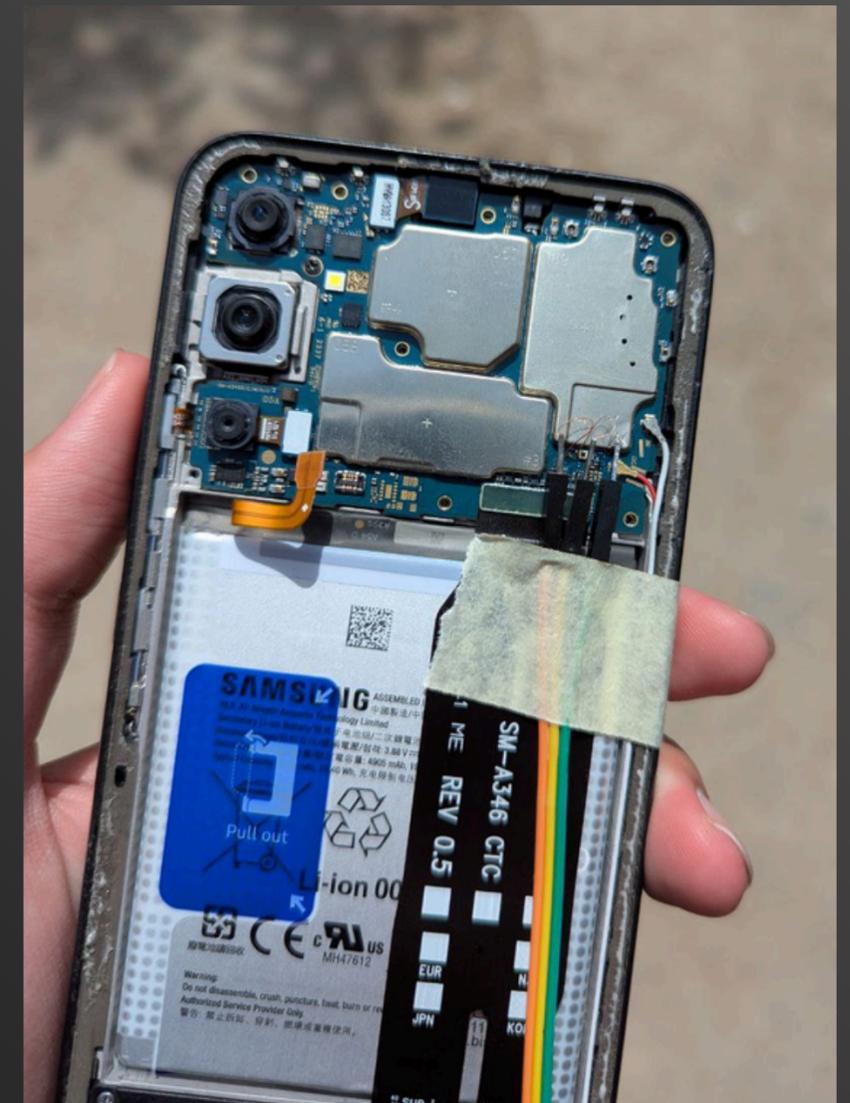
We had 2 ideas for an alternative:

- using pstore/ramoops
 - this turned out to not be possible as the DRAM gets wiped on every boot.
- framebuffer
 - this required fiddling with DSI after every pixel update, which I could not get working outside the LK bootloader quickly enough.

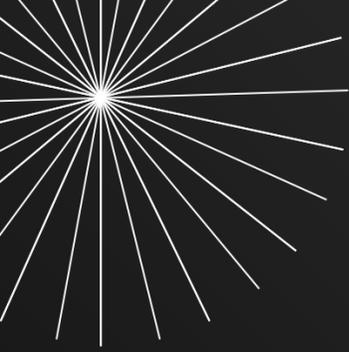


First steps towards becoming EOL-less

- After struggling for over 3 months, we felt hopeless.
- We had gotten absolutely nowhere with the Volla Community Days coming up, so I decided to make a decision.
- I went out and bought a Galaxy A34, a device with the same SoC as the Quintus. We had no schematics for it initially, getting hands on them took a few days.
- On the 12th of June, I successfully got UART on the Galaxy A34.



Galaxy A34 with UART on the board



First steps towards becoming EOL-less

- Now that we had another device, it was all-or-nothing. I spent multiple nights getting all of the core peripherals up.
- After 3 brutal nights, I had gotten core subsystems up for interfacing with hardware like PMICs and IOMMUs.
- Now we had only one more goal, functional USB. Unfortunately due to the lack of time on our part, we only managed to get the USB controller and USB regulators working. But the USB port **electrocutes** me now!

First steps towards becoming EOL-less

```
initializing kernel
====> GZ (0)[ 0.013092]INIT: cpu 0, calling hook 0x3800b3b0 (plat_sip_smc_ver) at level 0x50000, flags 0x1
====> GZ (0)[ 0.014288]plat_sip_smc_version_init sip ver, major=0x0, minor=0x2
====> GZ (0)[ 0.015181]plat_sip_smc_version_init sip smc remaked is supported!
====> GZ (0)[ 0.016075]INIT: cpu 0, calling hook 0x3803fc9c (vm) at level 0x50000, flags 0x1
====> GZ (0)[ 0.017126]INIT: cpu 0, calling hook 0x380091fc (libvmm) at level 0x50001, flags 0x1
====> GZ (0)[ 0.018220][platform_set_boot_mode]:gz boot mode is 1
====> GZ (0)[ 0.018977]platform_set_boot_info BOOT_TAG_IS_ABNORMAL_BOOT found=0x0!
[LOG]
[LOG]
[LOG]
[LOG]
[LOG]
[LOG]
[LOG]
[LOG] Passed board initialization!
[LOG] Welcome to uniLoader!
[LOG] Booting linux...
[ 0.000000] Booting Linux on physical CPU 0x0000000000 [0x412fd050]
[ 0.000000] Linux version 6.14.0-rc5-next-20250306-gbb9e831b0-dirty (thevancedgamer@ThinkStation-Arch) (aarch64-linux-gnu-gcc (GCC) 15.1.0, GNU
ld (GNU Binutils) 2.44) #39 SMP PREEMPT Sun Jun 15 02:03:47 PKT 2025
[ 0.000000] KASLR disabled due to lack of seed
[ 0.000000] Machine model: Volla Phone Quintus
[ 0.000000] printk: debug: skip boot console de-registration.
[ 0.000000] efi: UEFI not found.
[ 0.000000] OF: reserved mem: 0x0000000048090000..0x000000004816ffff (896 KiB) map non-reusable ramoops@48090000
[ 0.000000] OF: reserved mem: 0x0000000074780000..0x0000000075123f7f (9871 KiB) nomap non-reusable framebuffer@74780000
[ 0.000000] NUMA: Faking a node at [mem 0x0000000040000000-0x0000000023ffffff]
[ 0.000000] NODE_DATA(0) allocated [mem 0x23efdde00-0x23efe043f]
[ 0.000000] Zone ranges:
[ 0.000000] DMA [mem 0x0000000040000000-0x00000000ffffff]
[ 0.000000] DMA32 empty
[ 0.000000] Normal [mem 0x0000000010000000-0x0000000023ffffff]
[ 0.000000] Movable zone start for each node
[ 0.000000] Early memory node ranges
[ 0.000000] node 0: [mem 0x0000000040000000-0x000000007477ffff]
[ 0.000000] node 0: [mem 0x0000000074780000-0x0000000075122fff]
[ 0.000000] node 0: [mem 0x0000000075124000-0x0000000023ffffff]
```

Mainline Linux kernel booting on the Galaxy A34

First steps towards becoming EOL-less

```
~ # uname -a
Linux (none) 6.14.0-rc5-next-20250306-gbb9e831b0-dirty #39 SMP PREEMPT Sun Jun 15 02:03:47 PKT 2025 aarch64 Linux
~ # ls /
README      etc          proc          sysroot
bin          hooks-cleanup ramdisk       tmp
boot        init         root          usr
config      init_functions.sh run
d           lib          sbin
dev         pmOS_init.log sys
~ #
```

PostmarketOS debug initramfs working on
Galaxy A34

First steps towards becoming EOL-less

```
~ # dmesg | grep mtu3
[ 7.666584] mtu3 11201000.usb: supply vusb33 not found, using dummy regulator
[ 7.668616] mtu3 11201000.usb: supply vbus not found, using dummy regulator
[ 7.669368] mtu3 11201000.usb: dr_mode: 3, drd: auto
[ 7.670051] mtu3 11201000.usb: u2p_dis_msk: 0, u3p_dis_msk: 0
[ 7.674564] mtu3 11201000.usb: usb3-drd: 1
[ 7.675641] mtu3 11201000.usb: irq 161
[ 7.676266] mtu3 11201000.usb: IP version 0x1005(U3 IP)
[ 7.677473] mtu3 11201000.usb: max_speed: super-speed-plus
[ 7.678382] mtu3 11201000.usb: fifosz/epnum: Tx=0x2000/8, Rx=0x2000/8
[ 7.679223] mtu3 11201000.usb: dma mask: 36 bits
[ 12.765346] mtu3 11201000.usb: xHCI platform device register success...
[ 12.766667] mtu3 probe done successfully!
[ 12.766771] starting mtu3!
[ 12.784507] mtu3 11201000.usb: gadget (super-speed) pullup D+
~ #
```

MTU3 (dual role USB) working

First steps towards becoming EOL-less: Overall Experience

- Overall, MediaTek sounds like an easy platform to work on, but in practicality, that's definitely not the case. There are unrelated driver changes scattered around everywhere that makes rewriting the driver much more difficult, as all of the logic is spread apart with no easy way to piece it together.
- However, with enough effort and time, it is entirely possible, as shown in our PoC.



What works and what needs to be done

Working:

- Core subsystems
- Clocks
- Pinctrl
- USB
- PWRAP
- Main PMIC (MT6359)

TBD:

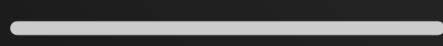
- Rest of the PMICs
- UFS
- Display (dispsys subsystem) and GPU
- ICCs, cpufreq, etc
- All phone-related features



Planned roadmap

Q2 2025

Initial bring up of basic core peripherals in a fork of ours



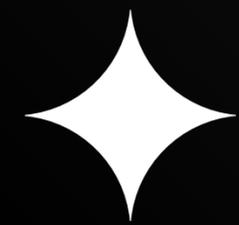
Q3/Q4 2025

Upstreamed basic support for MT6877 and Quintus, work on complex hardware blocks

Q4 2025 and onwards



If all goes well, more work on usability as a phone



Resources

[1]: <https://github.com/ivoszbg/uniLoader/commit/5ee9fd9c1a71168abfac4704e8bb8458a9cfb0dct>

[2]: <https://ivoszbg.xyz/blog/quintus-hacking-1/>

Q/A